

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

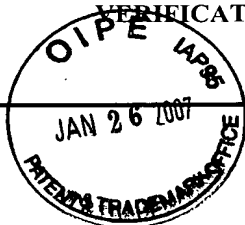
Docket No.
200-0667

2128
174

In Re Application Of: Joseph G. Walacavage et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
09/966,121	September 28, 2001	K. Gebresilassie	33481	2128	4437

Title: **METHOD OF EMULATING MACHINE TOOL BEHAVIOR FOR
PROGRAMMABLE LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM**



Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☒ the fee set forth in 37 CFR 1.17(p).

01/29/2007 MWOLDGE1 00000013 061510 09966121
01 FC:1806 180.00 DA

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
200-0667

In Re Application of: **Joseph G. Walacavage et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
09/966,121	September 28, 2001	K. Gebresilassie	33481	2128	4437

Title: **METHOD OF EMULATING MACHINE TOOL BEHAVIOR FOR
PROGRAMMABLE LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM**

JAN 26 2007

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 06-1510
as described below.
- ☒ Charge the amount of **\$180.00**
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Certificate of Transmission by Facsimile*

I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fa: _____)

(Date)

Signature

Typed or Printed Name of Person Signing Certificate

Certificate of Mailing by First Class Mail

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

January 22, 2007

(Date)

Signature of Person Mailing Correspondence

Daniel H. Bliss

Typed or Printed Name of Person Mailing Certificate

***This certificate may only be used if paying by deposit account.**

Signature

Dated: January 22, 2007

Daniel H. Bliss (Reg. No. 32,398) [0693.00258]
Bliss McGlynn, P.C.
2075 West Big Beaver Road, Suite 600
Troy, Michigan 48084
(248) 649-6090

Record I.D. 81047711

CC:

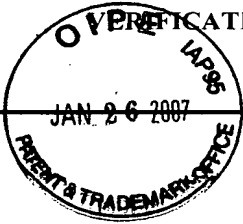
**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.
200-0667

In Re Application Of: **Joseph G. Walacavage et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
09/966,121	September 28, 2001	K. Gebresilassie	33481	2128	4437

Title: **METHOD OF EMULATING MACHINE TOOL BEHAVIOR FOR
PROGRAMMABLE LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM**



Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

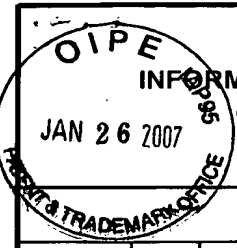
37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☒ the fee set forth in 37 CFR 1.17(p).

**INFORMATION DISCLOSURE CITATION***(Use several sheets if necessary)*

ATTY DOCKET NO.

200-0667

APPLICATION NO.

09/966.121

APPLICANT(S)

Joseph G. Walacavage et al.

FILING DATE

September 28, 2001

GROUP ART UNIT

2128

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,758,123	05/26/1998	Sano et al.			
	6,292,715	09/18/2001	Rongo			

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

Beck et al., "Applying a Component-Based Software Architecture to Robotic Workcell Applications". IEEE Transactions on Robotics and Automation, Vol. 16, No. 3, June 2000.

Frey et al. "Formal Methods in PLC Programming", IEEE International Conference on Systems, Man and Cybernetics, Nashville, Tennessee, October 2000.

Leduc, Ryan James, "PLC Implementation of DES Supervisor for a Manufacturing Testbed: An Implementation Perspective", Thesis submittal, Graduate Department of Computer and Electrical Engineering, University of Toronto, 1996, pages i-xv, 50-51, 129-143, obtained via <http://www.cas.mcmaster.ca/~leduc/rleduc-masc.pdf>.

EXAMINER	DATE CONSIDERED
----------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.